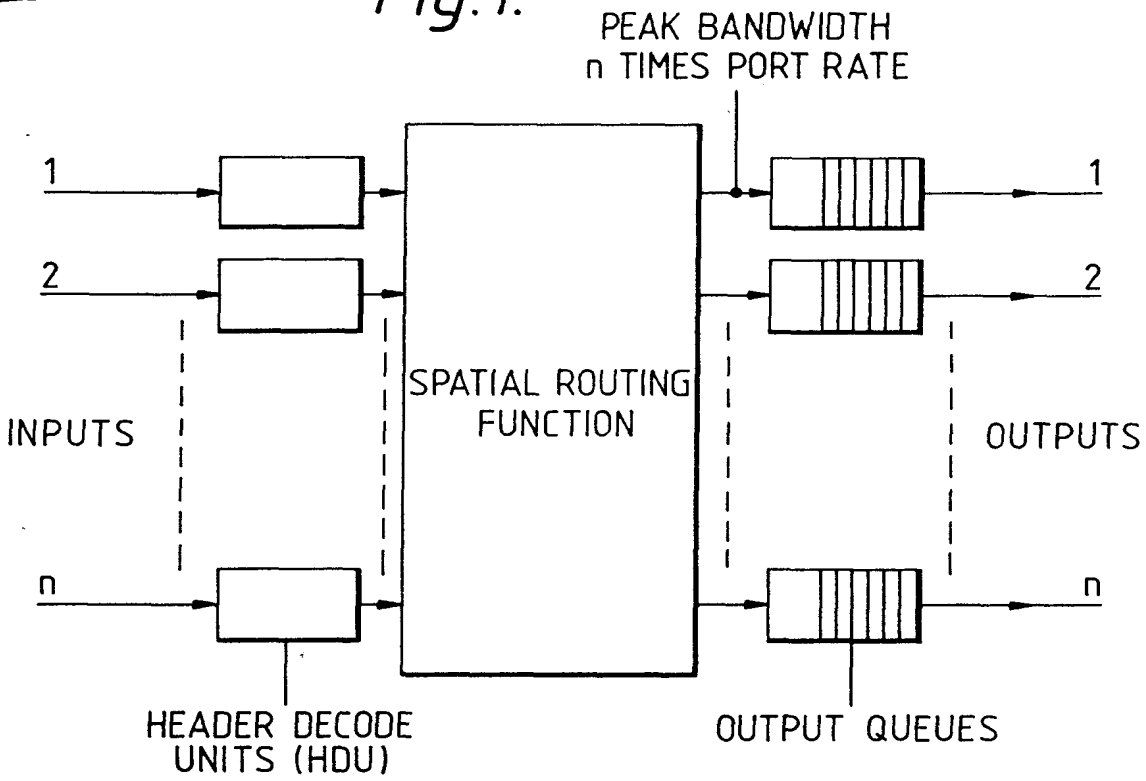
PRIOR ART

Fig. 1.



PRIOR ART

Fig. 3.

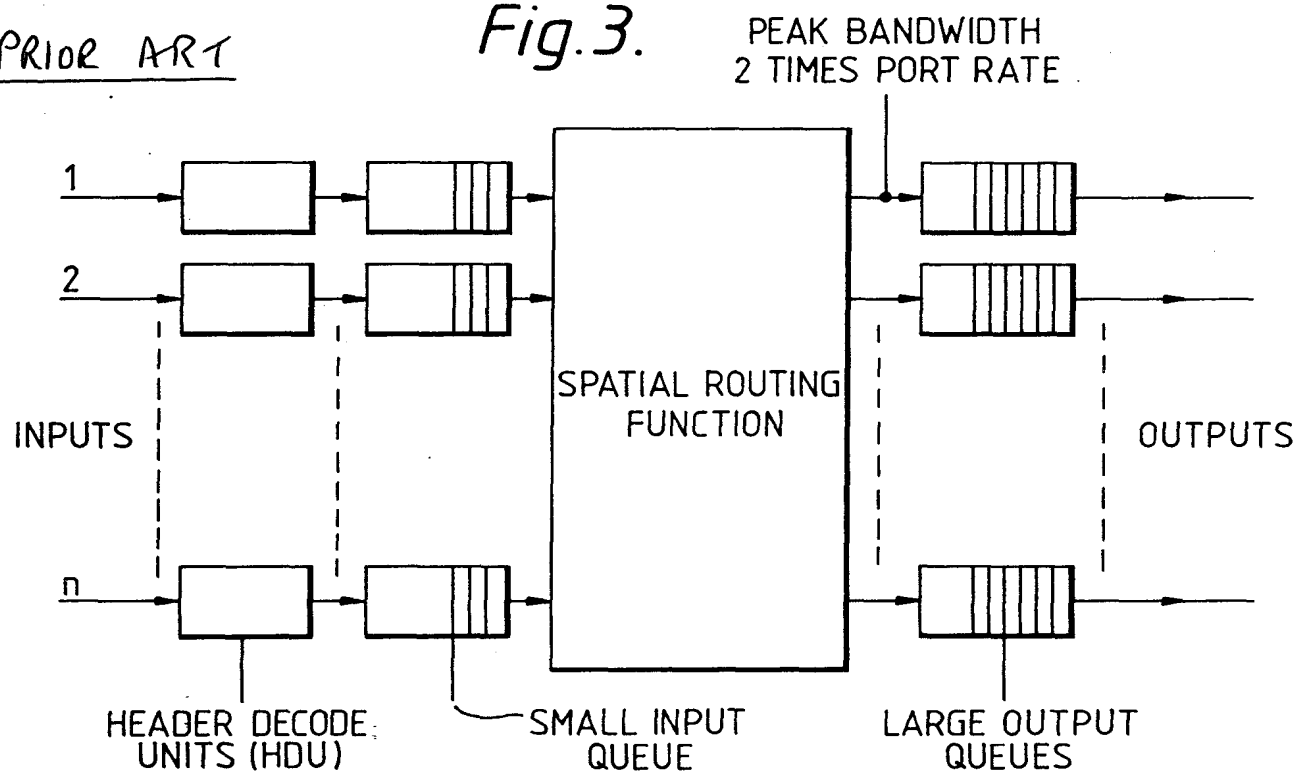
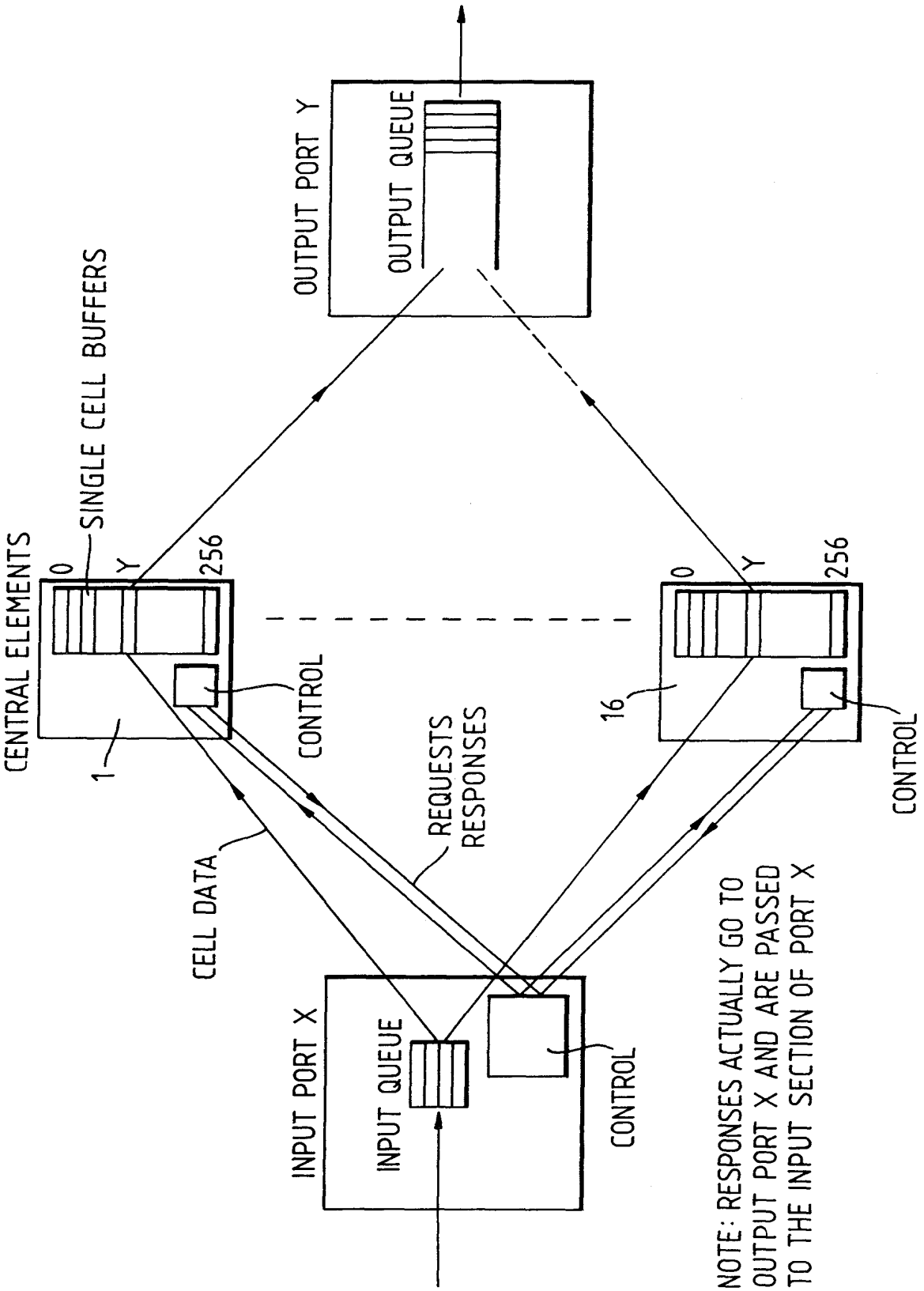


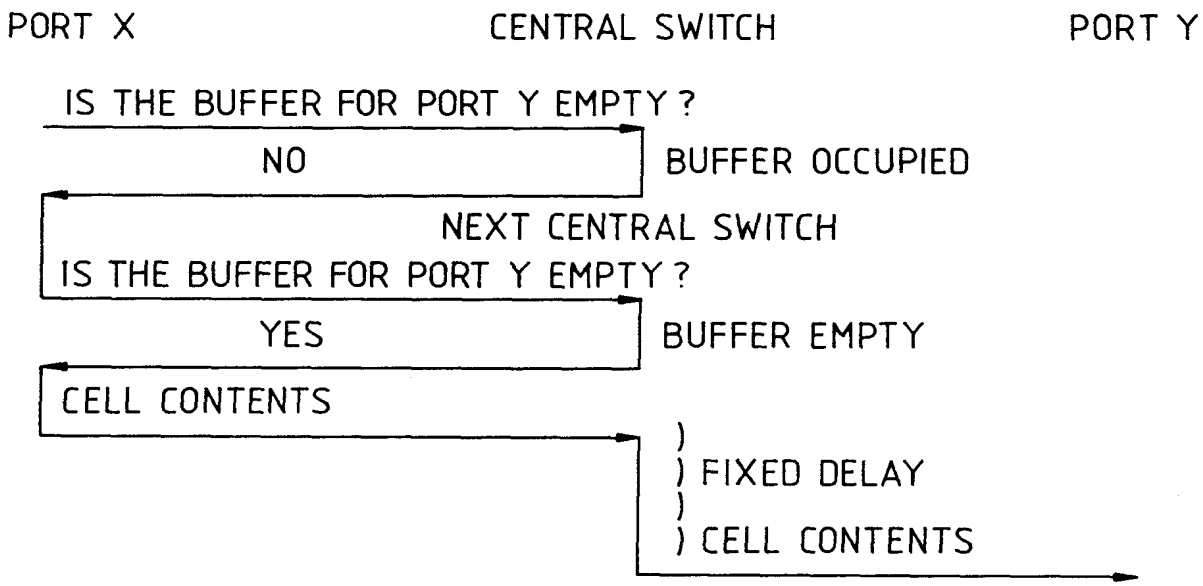
Fig. 2.

PRIOR ART



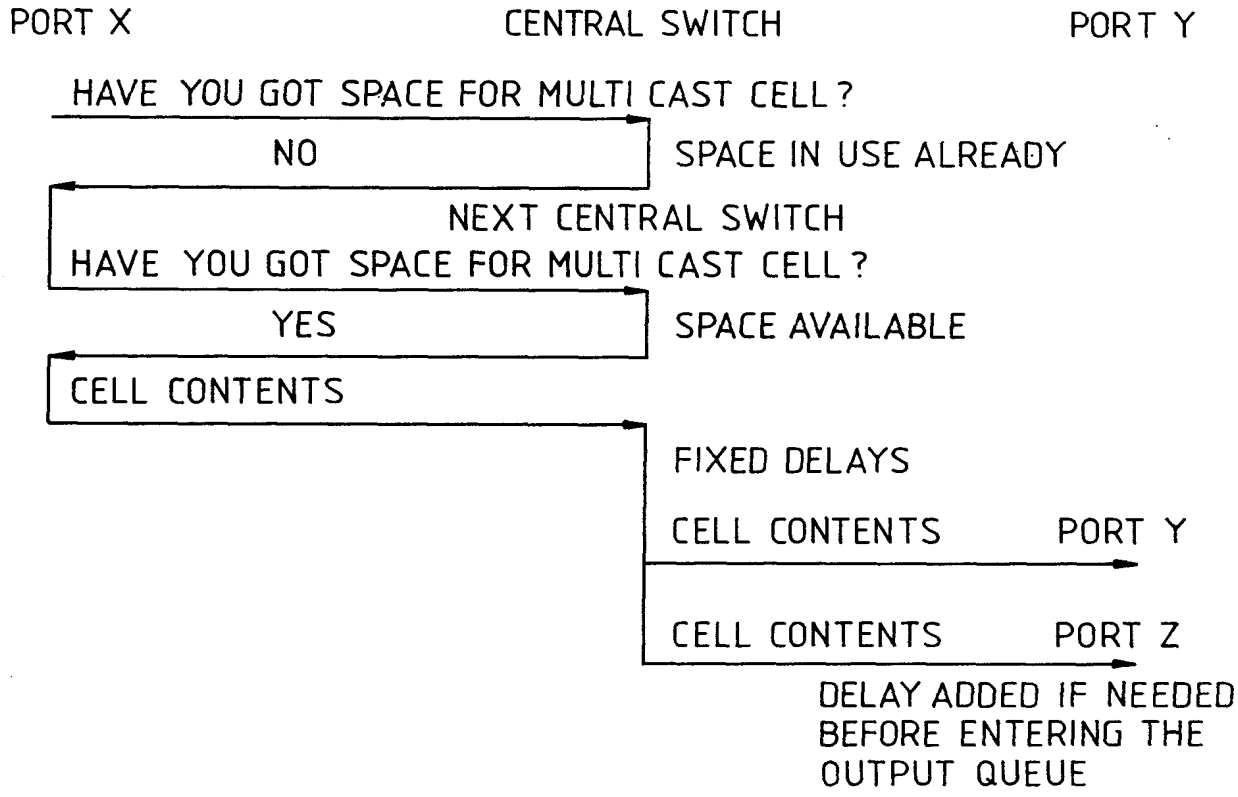
PRIOR ART

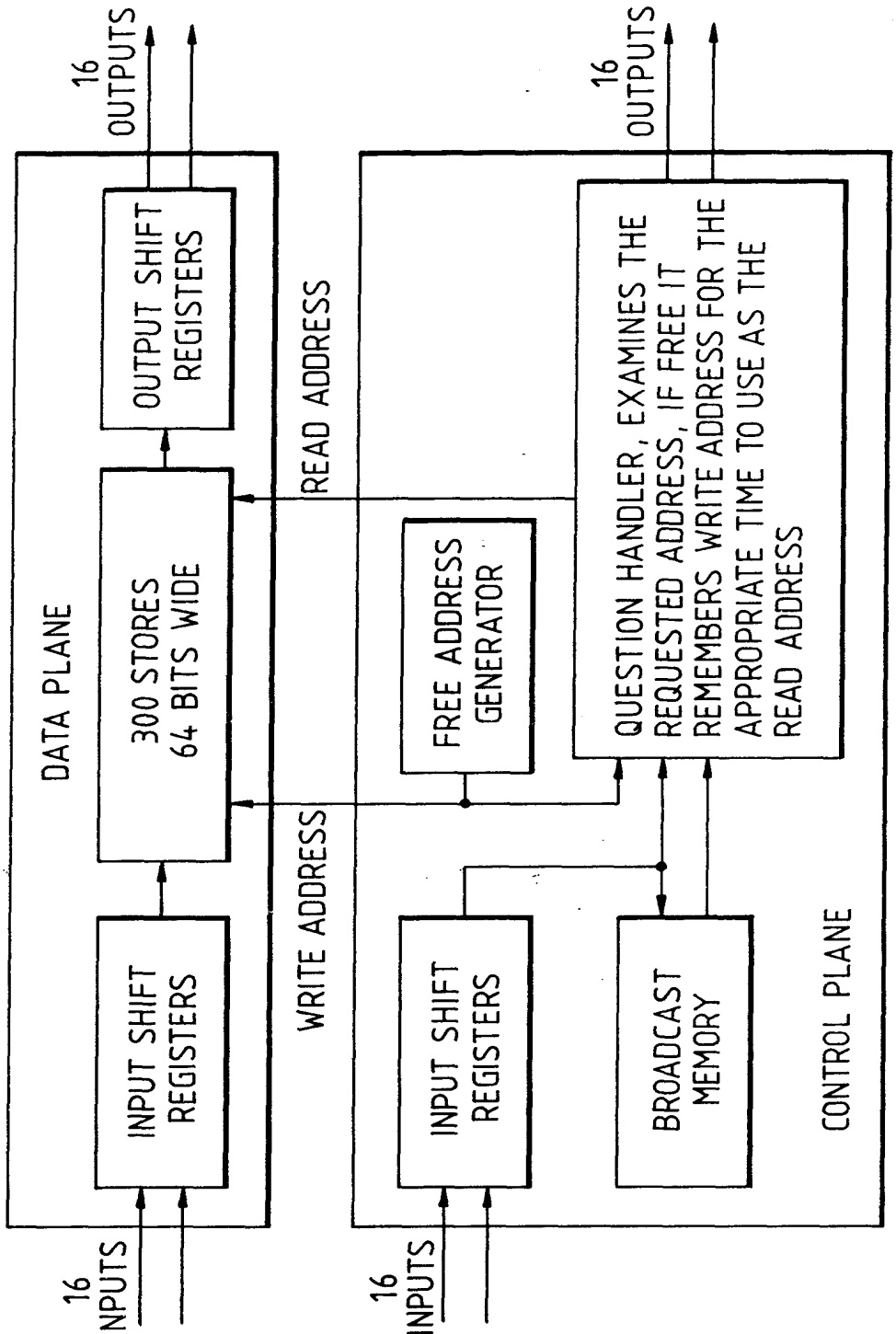
Fig. 4.



PRIOR ART

Fig. 5.



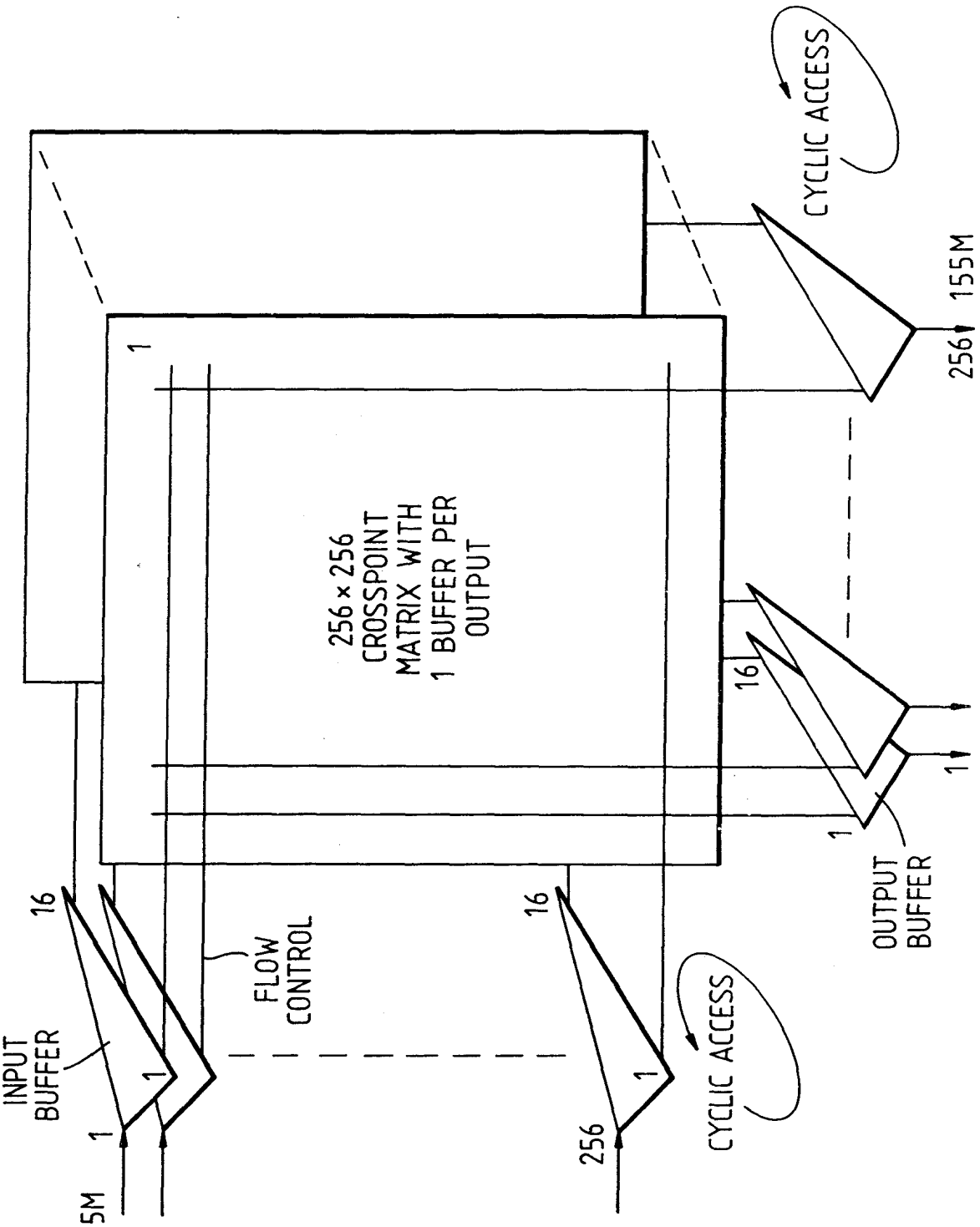


PRIOR ART

Fig. 6.

PRIOR ART

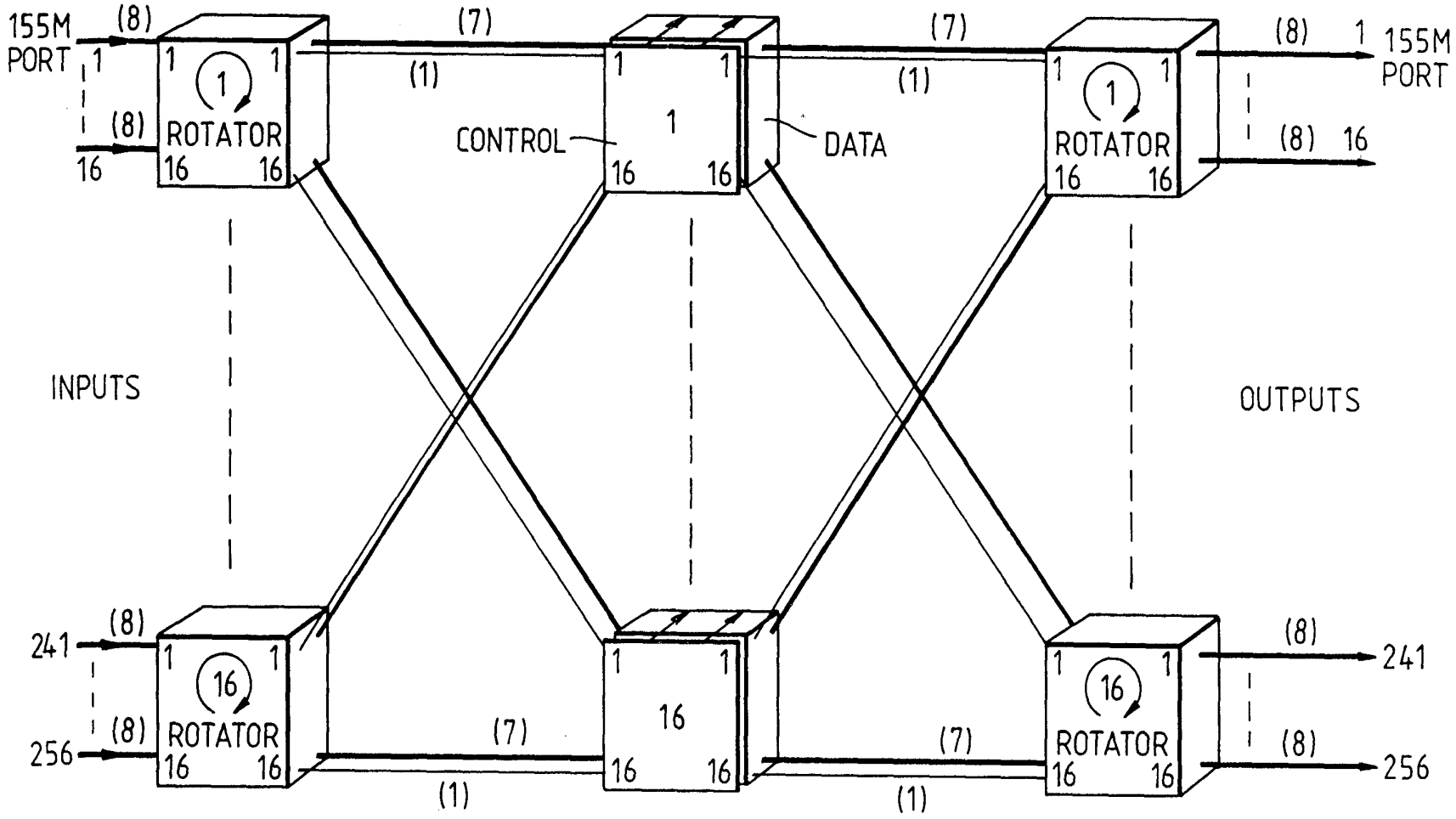
Fig. 7.



PRIOR ART

Fig.8.

NOTE : ANY NUMBER OF INPUT/OUTPUT "PORTS" CAN BE CONCATENATED WITH GUARANTEED CELL SEQUENCE INTEGRITY TO ALLOW EVOLUTION TO 600M, 2.4G, 9.6G ETC.



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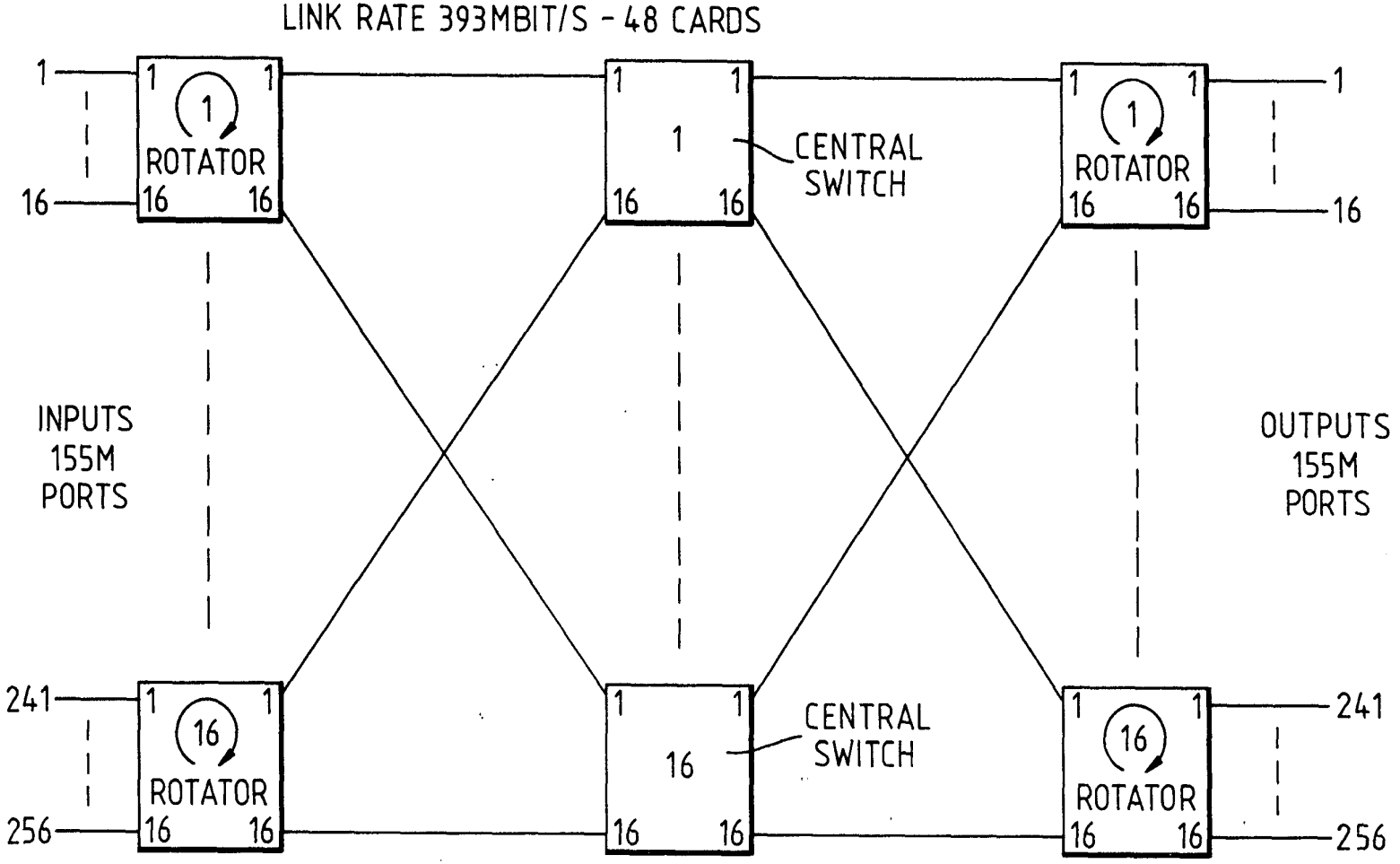
PRIOR ART

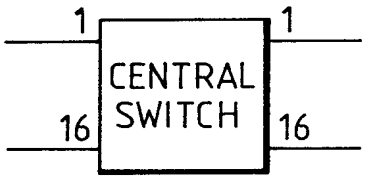
Fig. 9.



NOTE: OPTICAL ROTATORS HALVE THE NUMBER OF ROTATOR CARDS BY COMBINING Rx AND Tx FUNCTIONS.

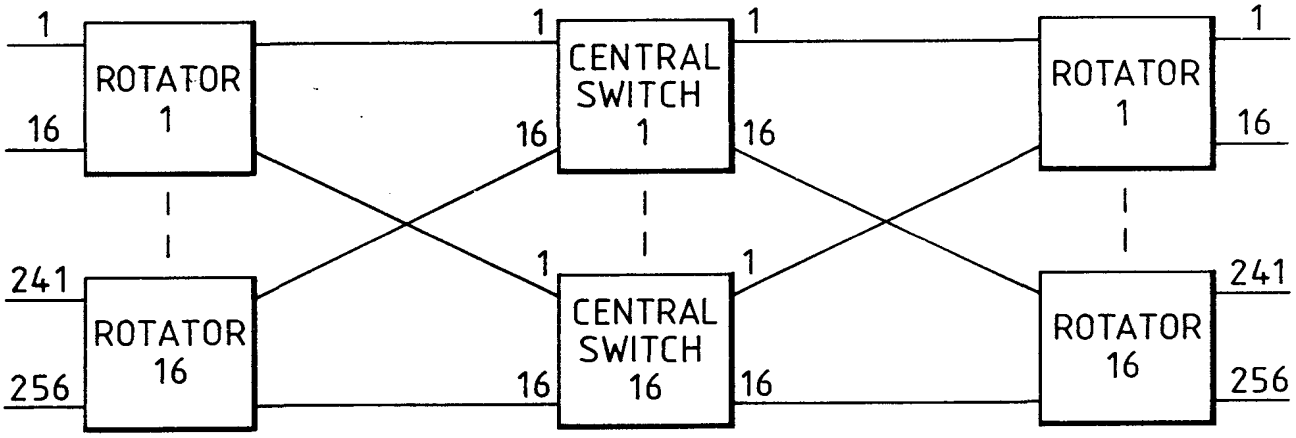
PRIOR ART

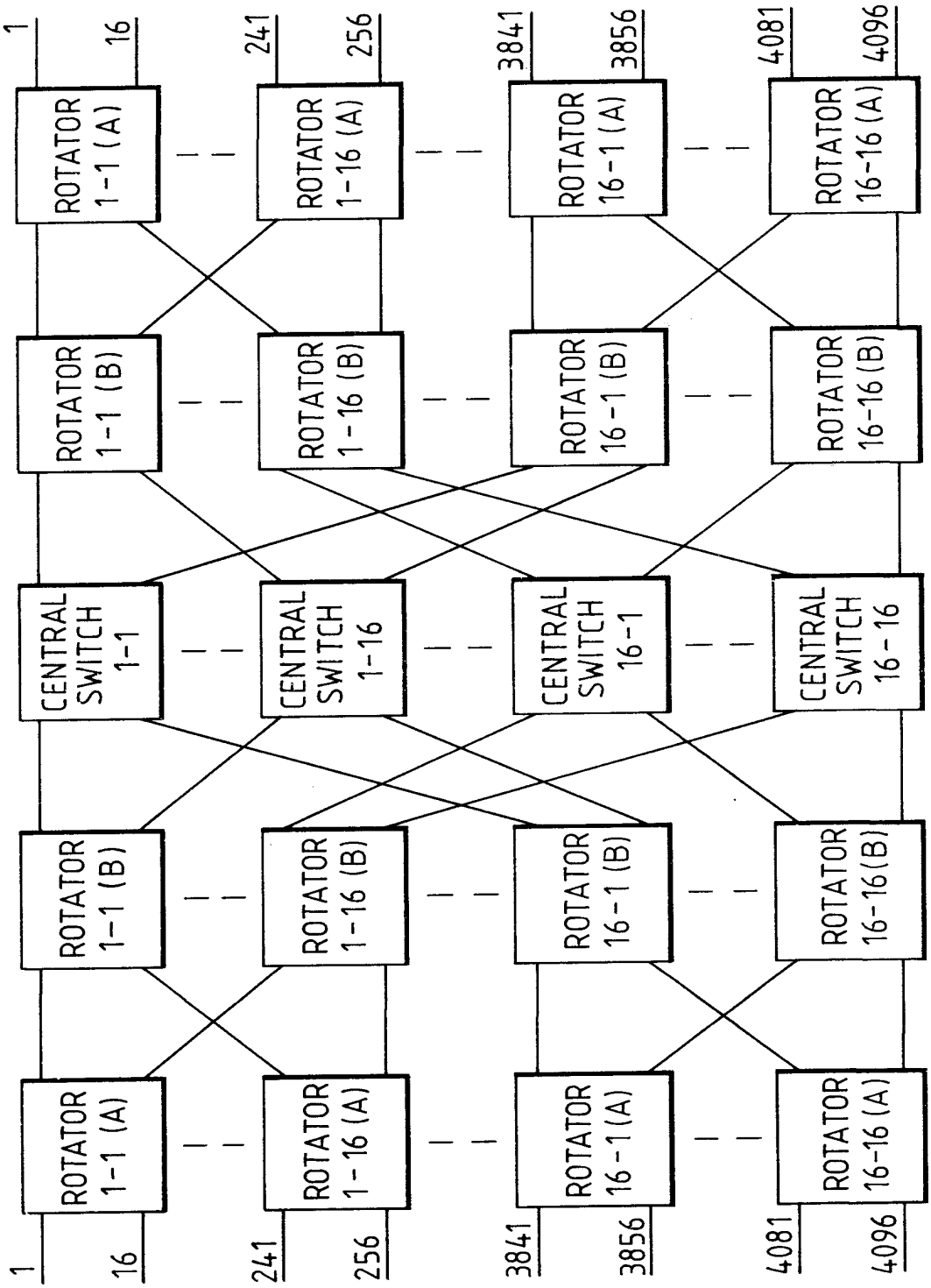
Fig. 10a.



PRIOR ART

Fig. 10b.



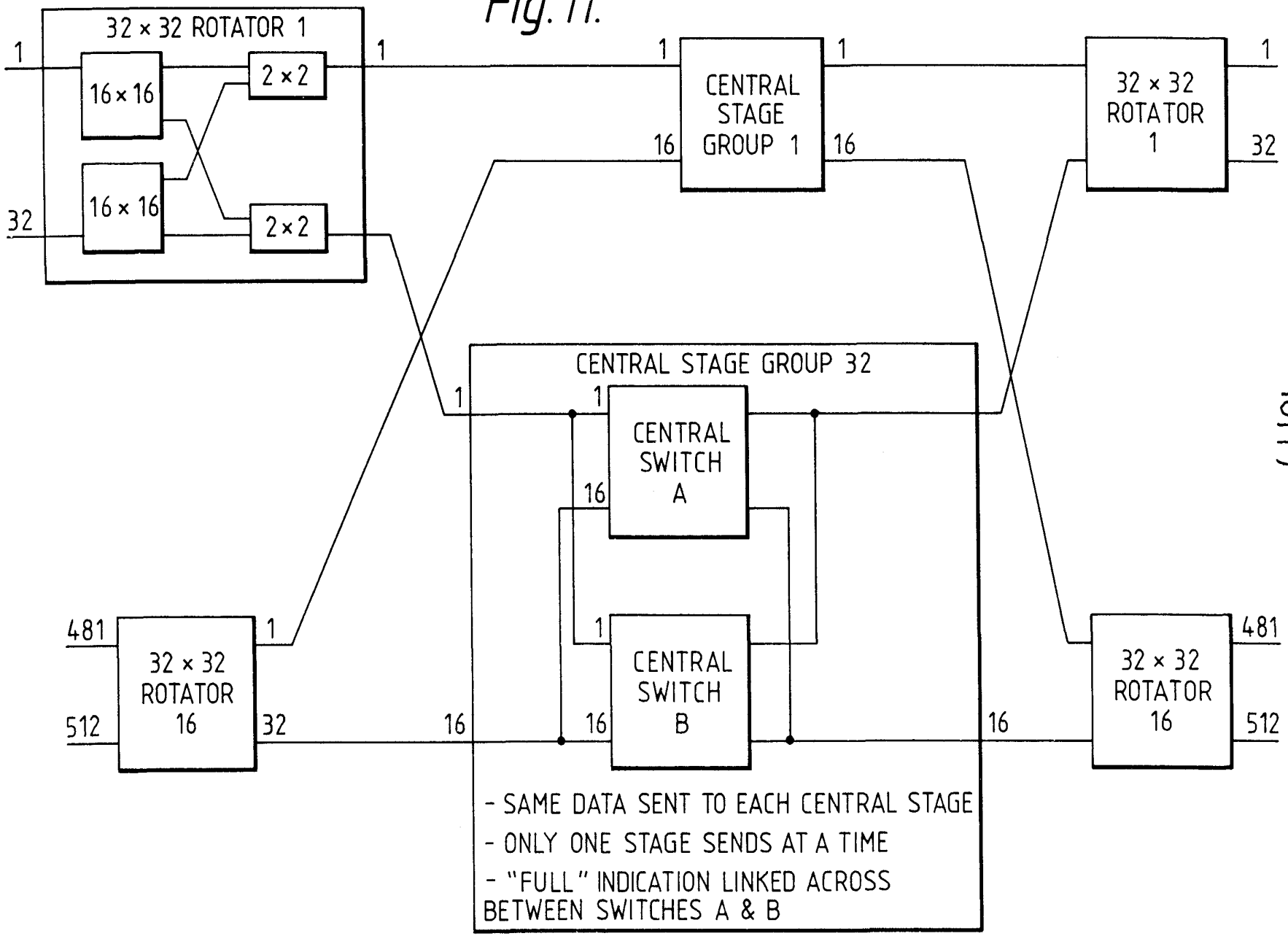


PRIOR ART

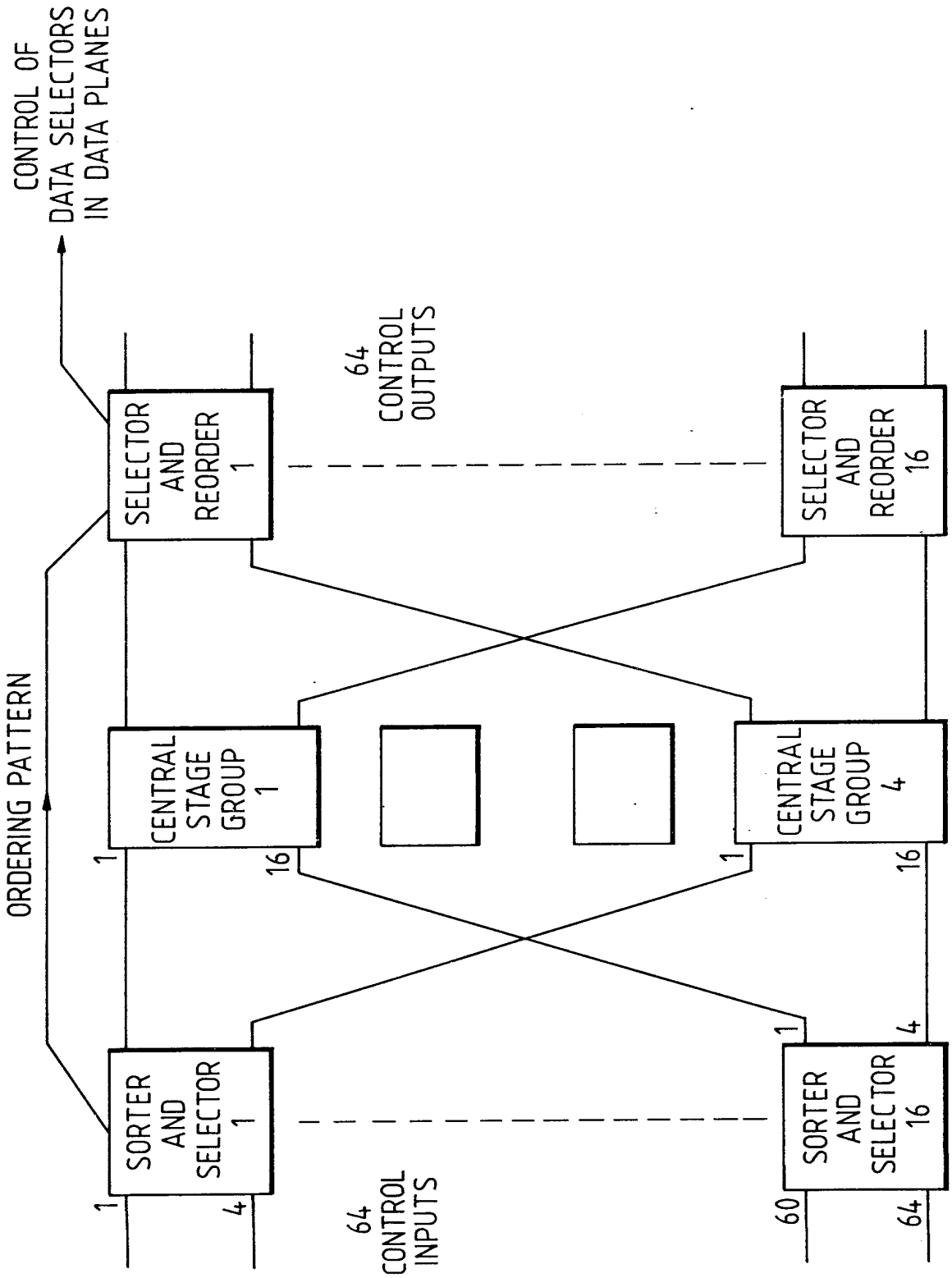
Fig. 10c.

PRIOR ART

Fig. 11.

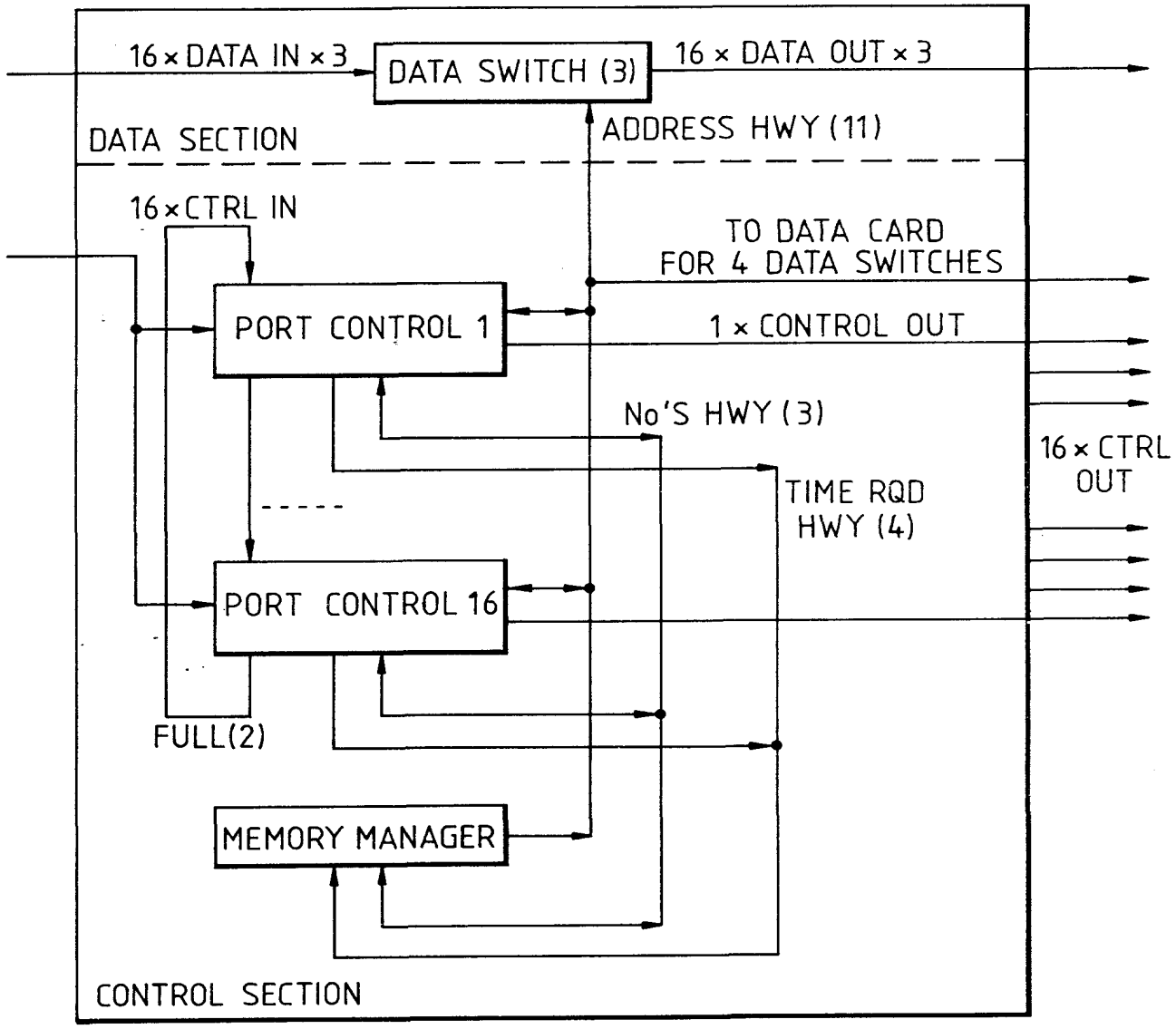


PRIOR ART Fig. 12.



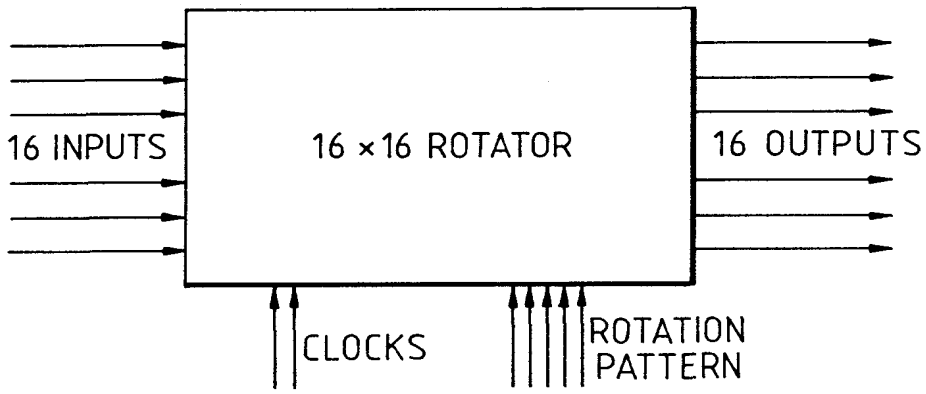
PRIOR ART

Fig. 13.



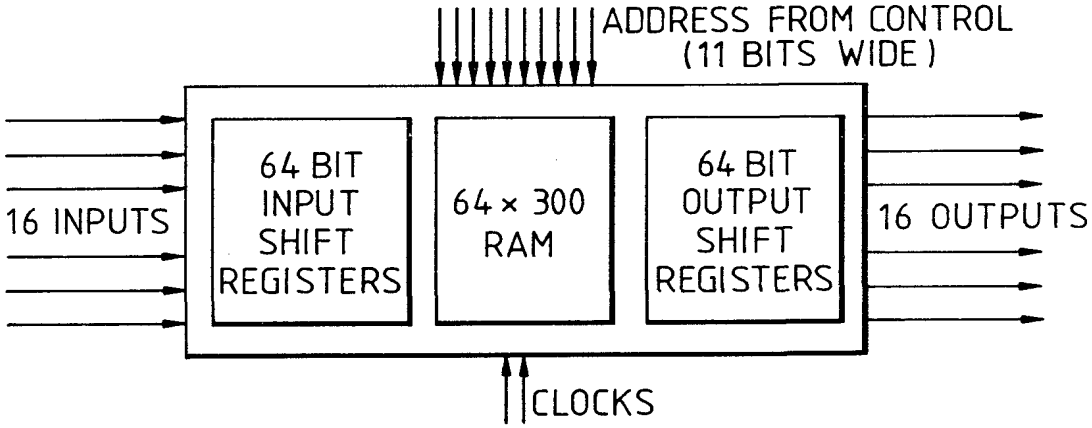
PRIOR ART

Fig. 14.



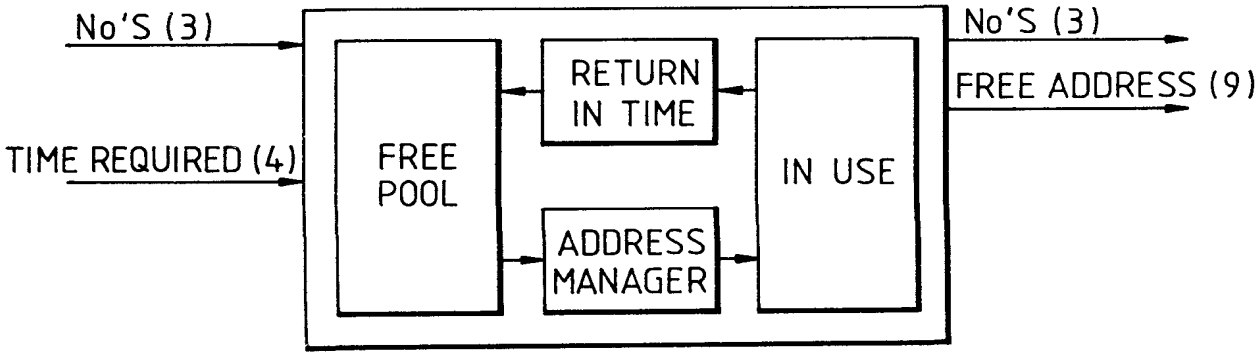
PRIOR ART

Fig. 15.



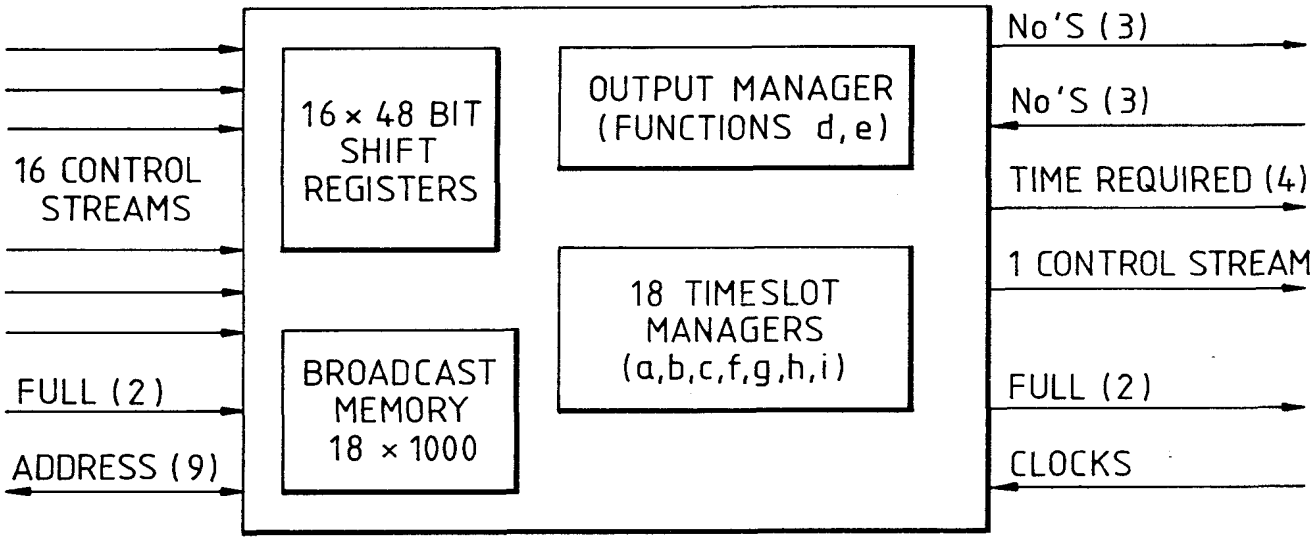
PRIOR ART

Fig. 16.



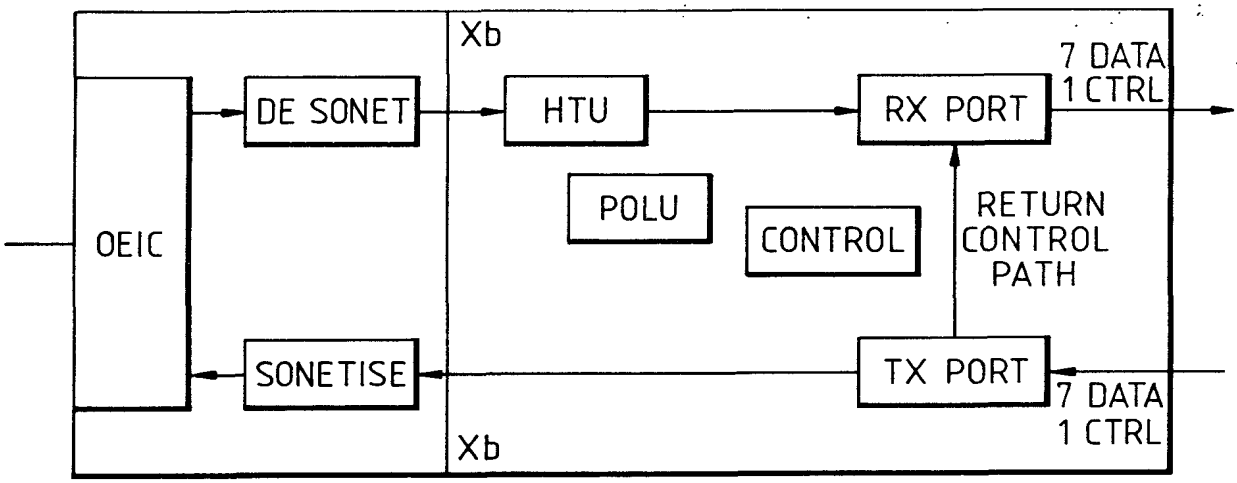
PRIOR ART

Fig. 17.



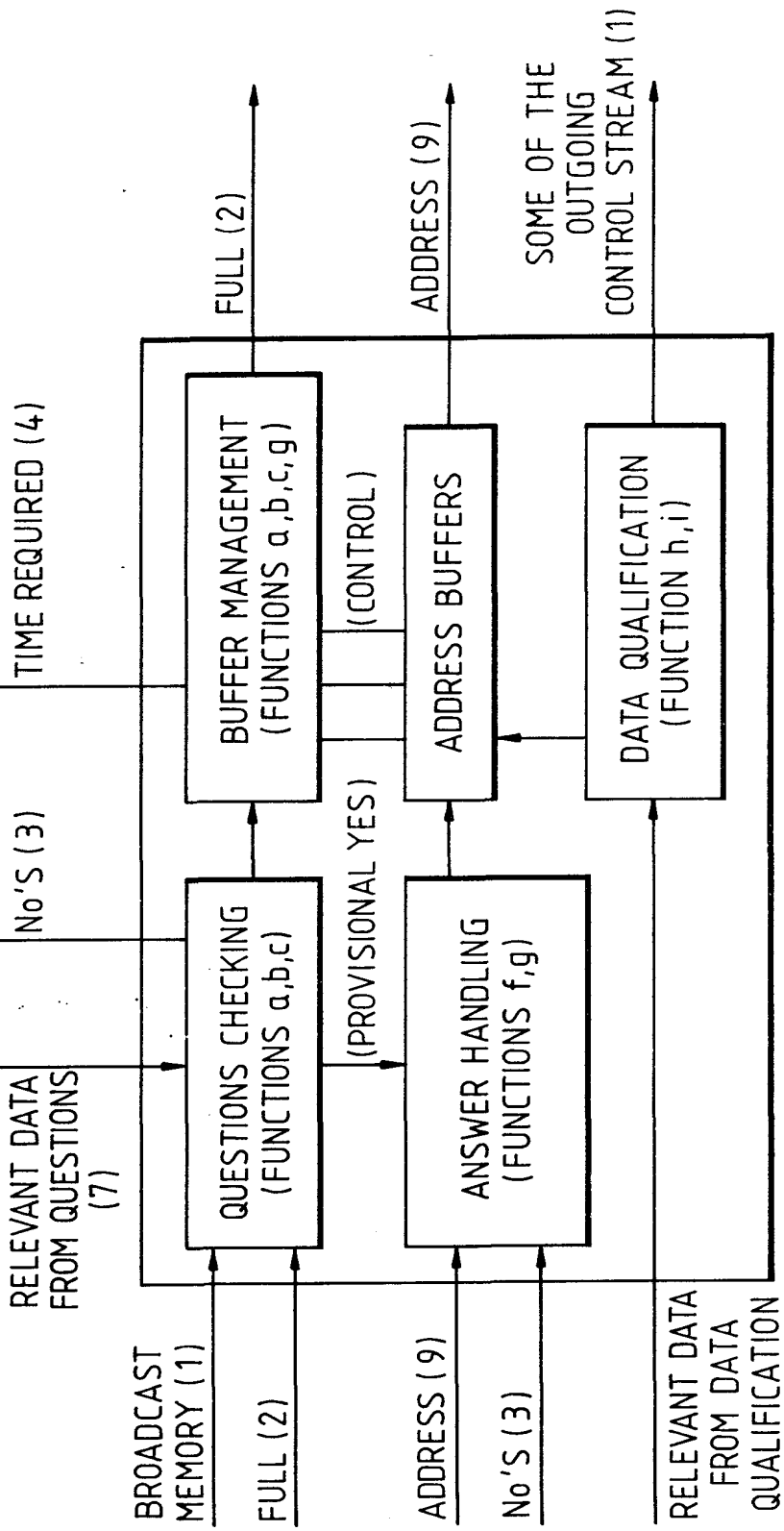
PRIOR ART

Fig. 19.



PRIOR ART

Fig. 18.



PRIOR ART

Fig. 20.

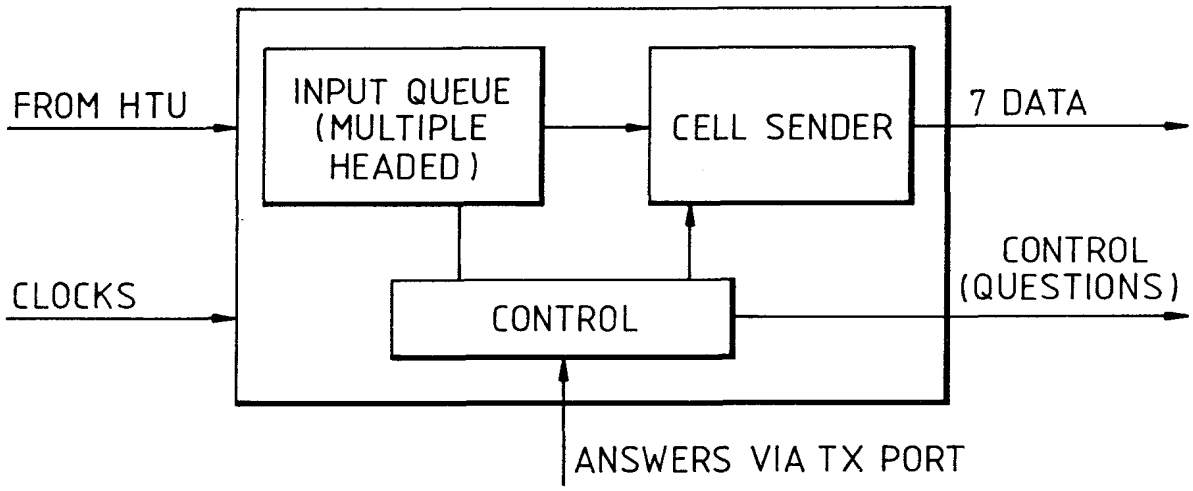


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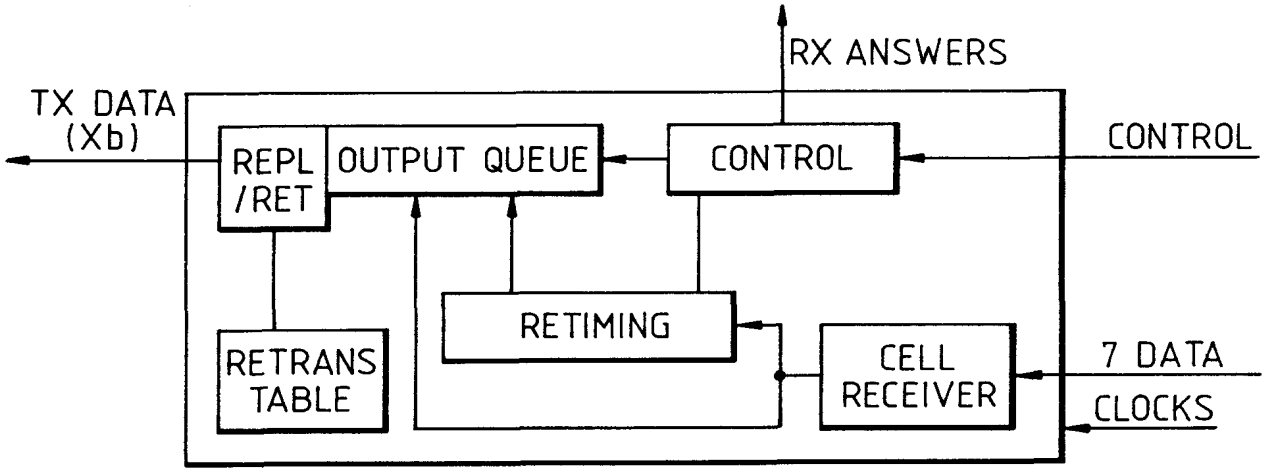
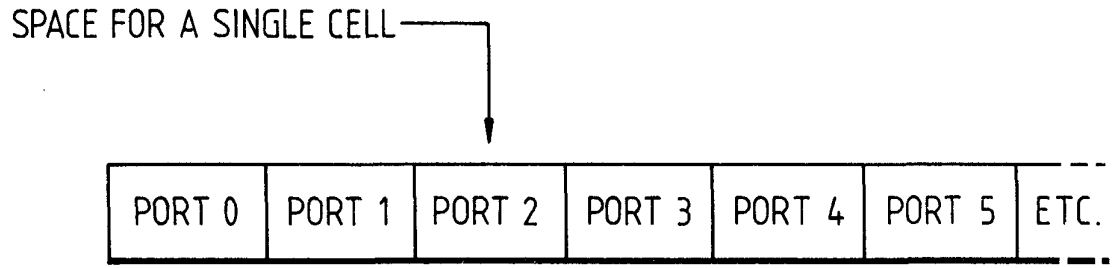
Fig. 21.



PRIOR ART

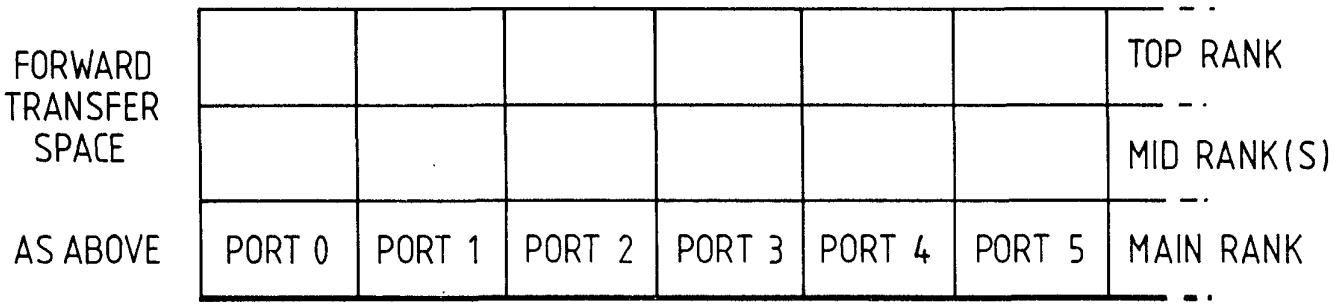
Fig.22A.



SINGLE CELL STORAGE, FOR POINT TO POINT CELLS

PRIOR ART

Fig.22B.



ADDITIONAL STORAGE FOR MULTIPOINT CELLS

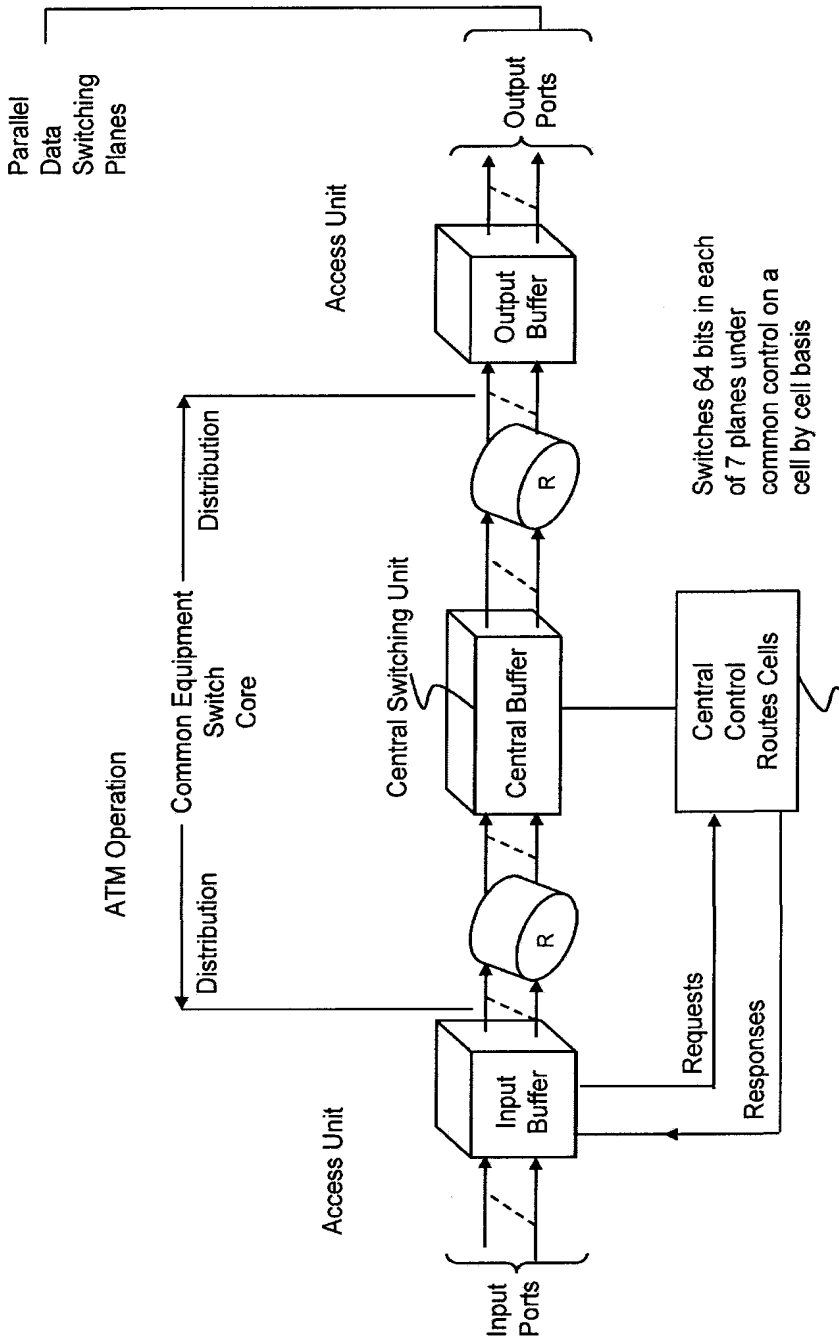


FIG. 23

PRIOR ART

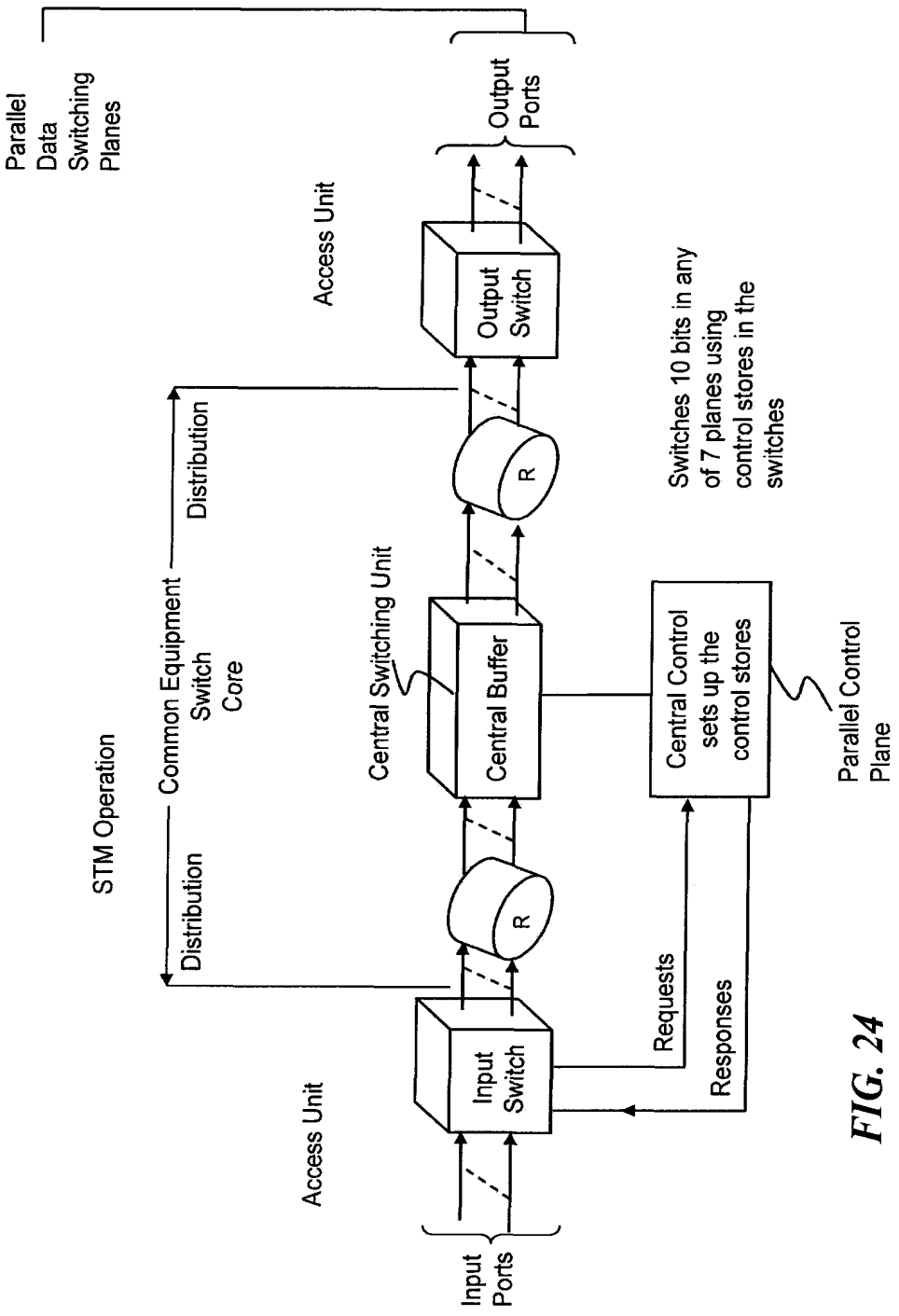

Parallel
Data
Switching
Planes
Output
Ports
Access Unit
Output
Switch
R
Distribution
Distribution
Common Equipment
Switch
Core
STM Operation
Central Switching Unit
Central Buffer
Switches 10 bits in any
of 7 planes using
control stores in the
switches
Central Control
sets up the
control stores
Parallel Control
Plane
Requests
Responses
R
Input
Switch
Access Unit
Input
Ports

FIG. 24